Docket No.: 51876P446

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:	
Min-Hyung Cho, et al.	Art Group:
Application No.:	Examiner:
Filed:	
For: Quadrature Modulation Transmitter	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

-1- 51876P446

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOMOLOFF, TAYLOR & ZAFMAN LLP

Eric S. Hyman, Reg. No. 30,139

Date: / \//\/\

12400 Wilshire Boulevard, 7th Floor Los Angeles, CA 90025 Telephone: (310) 207-3800 Substitute for form 1449A/PTO

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

Complete if Known

Application Number

Filing Date

First Named Inventor
Art Unit

Examiner Name

Attorney Docket Number

51876P446

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-6507625	01-14-2003	Iwamastsu et al.	
		US-6240142	05-29-2001	Kaufman et al.	
		US-			
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				NATENT DOCUMENTS	

		FOR	REIGN PATENT	DOCUMENTS		
Examiner	Cite	Foreign Patent Document			Pages Columns Lines	_
Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵

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Examiner	Date
Signature	Considered

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Substitute for form 1449A/PTO		Complete if Known		
INFORMATION DISCLOSURE			Application Number	
INFORMATION DISCLOSURE		Filing Date		
STATEMENT BY APPLICANT (use as many sheets as necessary)		First Named Inventor	Min-Hyung Cho	
		Art Unit		
		Examiner Name		
Sheet	C	of [Attorney Docket Number	51876P446

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
		"A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator fo rDigital Radio Applications, H. Samueli, et al., IEEE Journal, vol., 8, No. 8, Oct. 1990, pges 1512-1519		
		"A Digital Quadrature Modulator With On-Chip D/A Converter", J. Vankka, IEEE Journal of Solid-state Circuits, Vol, 38, No. 10, Oct. 2003, pgs 1635-1642		
		-		

Examiner	Date	
Signature	Considered	

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¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Information Disclosure Statement

New U.S. Patent Application for QUADRATURE MODULATION TRANSMITTER Our Ref. No.: P03EB025/US/js

Reference No.:

- (1) US Patent No. 6,507,625
- (2) US Patent No. 6,240,142
- (3) A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator for Digital Radio Applications (IEEE JOURNAL ON SELECTED AREAS IN COMMUNICATIONS, VOL. 8, NO. 8, OCTOBER 1990, PAGES 1512-1519)
- (4) A Digital Quadrature Modulator With On-Chip D/A Converter (IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 38, NO. 10, OCTOBER 2003, PAGES 1635-1642)